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Substitute for form 1449B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)			Complete If Known		
			Application Number	10/661,709	
			Filing Date	09/12/2003	
			First Named Inventor	Blauvelt et al.	
			Art Unit	2878	
			Examiner Name	Suezu Y Ellis	
Sheet	2	of	3	Attorney Docket Number	XPNT22NP

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
SYE		BAZYLENKO ET AL, Fabrication of Light-Turning Mirrors in Buried-Channel silica Waveguides for Monolithic and Hybrid Integration, Journal of Lightwave Technology, 01/1997, Page(s) 148-153, Volume 15, Number 1	
SYE		BOUADMA ET AL, 1.3-um GaInAsP/InP Buried-Ridge-Structure Laser and its Monolithic Integration with Photodetector Using RI Beam Etching, Journal of Lightwave Technology, 05/1994, Page(s) 742-748, Volume 12, Number 5	
SYE		CHAO ET AL, Fresnel Analysis of Effective Mirror Reflectivity in Folded-Cavity In-Plane Surface-Emitting Lasers, IEEE Photonics Technology Letters, 04/1993, Page(s) 390-392, Volume 4, Number 4	
SYE		FUKANO ET AL, A Low-Cost Edge-Illuminated Refracting-Facet Photodiode Module with Large Bandwidth and High Responsivity, Journal of Lightwave Technology, 01/2000, Page(s) 79-83, Volume 18, Number 1	
SYE		FUKANO ET AL, High-Responsivity and Low-Operation-Voltage Edge-Illuminated Refracting-Facet Photodiodes with Large Alignment Tolerance, Journal of Lightwave Technology, 05/1997, Page(s) 894-899, Volume 15, Number 5	
SYE		FUKANO ET AL, Edge-illuminated refracting-facet photodiode with high responsivity and low-operation voltage, Electronics Letters, 12/05/1996, Page(s) 2346-2348, Volume 32, Number 25	
SYE		GFELLER ET AL, 50 mW CW-Operated Single-Mode Surface-Emitting AlGaAs Lasers with 45 deg Total Reflection Mirrors, IEEE Photonics Technology Letters, 07/1992, Page(s) 698-700, Volume 4, Number 7	
SYE		HILLERINGMANN ET AL, Optoelectronic System Integration on Silicon: Waveguides, Photodetectors, and VLSI CMOS Circuits on One Chip, IEEE Transactions on Electron Devices, 05/1995, Page(s) 841-846, Volume 42, Number 5	
SYE		JONES ET AL, Hybrid integration onto silicon motherboards with planar silica waveguides, IEE Proc. Optoelectron., 10/1996, Page(s) 316-321, Volume 143, Number 5	
SYE		KATO ET AL, Large Coupling Tolerance Side-Illuminated Mirror Photodiode for Low-Cost Surface Hybrid Integration, IEEE Photonics Technology Letters, 06/1999, Page(s) 709-711, Volume 11, Number 6	
SYE		STRANDMAN ET AL, Fabrication of 45 deg Mirrors Together with Well-Defined V-Grooved Using Wet Anisotropic Etching of Silicon, Journal of Microelectromechanical Systems, 12/1995, Page(s) 213-219, Volume 4, Number 4	

Examiner Signature	<i>Suezu Y Ellis</i>	Date Considered	17 OCT 05
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Applicant's unique citation designation number (optional). *Applicant is to place a check mark here if English language Translation is attached.

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SYE		TERUI ET AL, Novel Micromirror for Vertical Optical Path Conversion Formed in Silica-Based PLC Using Wettability Control of Resin, Journal of Lightwave Technology, 09/1998, Page(s) 1631-1639, Volume 16, Number 9	
SYE		ZURHELLE ET AL, Highly Efficient Waveguide-Detector Coupling Structures for Integrated Opto-Electrical Circuits on Silicon, Journal of Lightwave Technology, 03/1996, Page(s) 410-416, Volume 14, Number 3	

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